

Day : Tuesday
Date: 11/1/2005

Time: 15:29:07

PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = PROPP

First Name = MICHAEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09837760</u>	Not Issued	120	04/17/2001	Wideband communication using delay line clock multiplier	PROPP, MICHAEL
<u>60198143</u>	Not Issued	159	04/17/2000	Wideband communication using delay line clock multiplier	PROPP, MICHAEL
<u>60198144</u>	Not Issued	159	04/17/2000	Token passing arrangement for power line communication system	PROPP, MICHAEL
<u>60198147</u>	Not Issued	159	04/17/2000	Wideband communication using delay line clock multiplier	PROPP, MICHAEL
<u>10333581</u>	Not Issued	30	11/18/2003	Power line communication network	PROPP, MICHAEL B
<u>10486248</u>	Not Issued	20	12/30/2004	Error correction process and mechanism	PROPP, MICHAEL B
<u>10488085</u>	Not Issued	41	08/04/2004	Communicating data using wideband communications	PROPP, MICHAEL B
<u>10488179</u>	Not Issued	30	11/03/2004	Communication data using wideband communications	PROPP, MICHAEL B
<u>10486243</u>	Not Issued	30	08/04/2004	Digital equalization process and mechanism	PROPP, MICHAEL B.
<u>60311081</u>	Not Issued	159	08/10/2001	Digital equalization process and mechanism	PROPP, MICHAEL B.
<u>60315950</u>	Not Issued	159	08/31/2001	Varying load and modulation applied to each of multiple frequency subchannels based on anticipated attenuation experienced by those subchannels	PROPP, MICHAEL B.
<u>06852788</u>	<u>4815106</u>	150	04/16/1986	POWER LINE COMMUNICATION APPARATUS	PROPP, MICHAEL B.
<u>08404627</u>	<u>5727004</u>	150	03/14/1995	METHOD AND APPARATUS FOR DATA ENCODING AND	PROPP, MICHAEL B.

COMMUNICATION OVER NOISY MEDIA					
<u>08682197</u>	<u>5774526</u>	150	07/17/1996	RECONFIGURABLE ON-DEMAND TELEPHONE AND DATA LINE SYSTEM	PROPP, MICHAEL B.
<u>08876385</u>	<u>5944842</u>	150	06/17/1997	METHOD AND APPARATUS FOR DATA ENCODING AND COMMUNICATION OVER NOISY MEDIA	PROPP, MICHAEL B.
<u>08877414</u>	<u>5872791</u>	150	06/17/1997	METHOD AND APPARATUS FOR DATA ENCODING AND COMMUNICATION OVER NOISY MEDIA	PROPP, MICHAEL B.
<u>60001476</u>	Not Issued	159	07/18/1995	RECONFIGURABLE ON-DEMAND TELEPHONE AND DATA LINE SYSTEM	PROPP, MICHAEL B.
<u>60310824</u>	Not Issued	159	08/09/2001	Error correction process and mechanism	PROPP, MICHAEL B.
<u>09539841</u>	<u>6823398</u>	150	03/31/2000	FILE SYSTEM MANAGEMENT EMBEDDED IN A STORAGE DEVICE	PROPPS, MICHAEL B.
<u>09583133</u>	Not Issued	93	05/30/2000	METHOD AND APPARATUS FOR EMULATING READ/WRITE FILE SYSTEM ON A WRITE-ONCE STORAGE DISK	PROPPS, MICHAEL B.
<u>09583390</u>	Not Issued	161	05/30/2000	Defect management system for write-once storage disk	PROPPS, MICHAEL B.
<u>09583448</u>	<u>6738333</u>	150	05/30/2000	FORMAT FOR RECORDING DATA IN A STORAGE DISK	PROPPS, MICHAEL B.
<u>09951333</u>	Not Issued	61	09/10/2001	System and method for controlling operation of a disc drive for optical media with premastered and read/write sectors	PROPPS, MICHAEL B.
<u>09951931</u>	Not Issued	61	09/10/2001	System and method for handling commands in an optical disc drive	PROPPS, MICHAEL B.
<u>10293893</u>	Not Issued	160	11/12/2002	Method and apparatus for emulating read/write file system on a write-once data storage disk	PROPPS, MICHAEL B.
<u>10299950</u>	<u>6912189</u>	150	11/18/2002	Skip list management for a write-once optical disk	PROPPS, MICHAEL B.

Inventor Search Completed: No Records to Display.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = JAKSON

First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09837760	Not Issued	120	04/17/2001	Wideband communication using delay line clock multiplier	JAKSON, JOHN
10333581	Not Issued	30	11/18/2003	Power line communication network	JAKSON, JOHN
10486248	Not Issued	20	12/30/2004	Error correction process and mechanism	JAKSON, JOHN
60198143	Not Issued	159	04/17/2000	Wideband communication using delay line clock multiplier	JAKSON, JOHN
60198147	Not Issued	159	04/17/2000	Wideband communication using delay line clock multiplier	JAKSON, JOHN
60310824	Not Issued	159	08/09/2001	Error correction process and mechanism	JAKSON, JOHN

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name
	<input type="text" value="JAKSON"/>	<input type="text" value="JOHN"/>
		<input type="button" value="Search"/>

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	("5727004").PN.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/06/24 20:13
S2	44570	(oscillator or clock) same inverter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:14
S3	28265	(oscillator or clock) with inverter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:14
S4	1552	((oscillator or clock) with inverter) with (((feed or fed) near back) or feedback)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:18
S5	8120	inverter.drwd.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:18
S6	189	(((oscillator or clock) with inverter) with (((feed or fed) near back) or feedback)) and inverter. drwd.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:19
S7	32586	(oscillator or clock).drwd.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:19
S8	100	(((oscillator or clock) with inverter) with (((feed or fed) near back) or feedback)) and inverter. drwd.) and ((oscillator or clock). drwd.)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:19
S9	15	((((oscillator or clock) with inverter) with (((feed or fed) near back) or feedback)) and inverter. drwd.) and ((oscillator or clock). drwd.)) and (inverter near series)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:35
S10	2343	375/371-373.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:31
S11	535	375/371-373.ccls. and ((oscillator or clock).drwd.)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:31

S12	4	(375/371-373.ccls. and ((oscillator or clock).drwd.)) and inverter. drwd.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:34
S13	5177	ring near oscillator	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:34
S14	44326	375/130-377.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:34
S15	126	((oscillator or clock) same inverter) and (ring near oscillator) and 375/130-377.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:34
S16	10	(((oscillator or clock) same inverter) and (ring near oscillator) and 375/130-377.ccls.) and (inverter near series)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/24 20:36
S17	10	(ring near oscillator) and ((inverter near series) with buffer)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/25 10:57
S18	78	bluetooth and (((frequency or channel) near hopping) with sequence)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/25 10:57
S19	5	("4716573" "5506863" "5528622" "5778075" "5809059").PN.	USPAT	OR	OFF	2004/06/25 11:00
S20	5963	dll	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:13
S21	50063	dll or (delay near line)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:14
S22	184065	voltage near control\$5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:14
S23	41671	(voltage near control\$5) same phase	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:15

S24	19002	(over near samp1\$6) oversamp1\$6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:14
S25	169	S21 and S23 and S24	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:15
S26	936	(voltage near control\$5) same phase same S21	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:15
S27	30	S26 and S24	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/10 17:15
S28	11	("4994730" "5180995" "5185538" "5440514" "5440515" "5442629" "5479124" "5563526" "5568064" "5608876" "5687330").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/10 17:37
S29	2	("6249164").URPN.	USPAT	OR	OFF	2005/02/10 17:41
S30	26	("5034813" "5185538" "5394114" "5440514" "5440515" "5442629" "5479124" "5537068" "5563526" "5568064" "5604775" "5608876" "5687202" "5687330" "5740213" "5818270" "5990714" "6037812" "6043717" "6087868" "6104223" "6249164" "6317161" "6330034" "6348823" "6392458").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:48
S31	19002	(over near samp1\$6) oversamp1\$6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 11:48
S32	11	(delay with dac) same S31	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:48
S33	0	(delay3 with dac) same S31	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:48
S34	17	(delay\$3 with dac) same S31	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:56

S35	813	S31 and delay\$4 and dac	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:56
S36	5812	S31 and delay\$4 and (dac samp1\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:57
S37	759	S31 and (delay\$4 near line) and (dac samp1\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/02/11 11:57
S38	33	S31 and ((delay\$4 near line) same inverter) and (dac samp1\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:00
S39	36	S31 and ((delay\$4 near line) same inverter) and (dac latch switch samp1\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:00
S40	841	S31 and (delay\$4 near line) and (dac latch switch samp1\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:00
S41	797	S31 and (delay\$4 near line) and ((dac latch switch samp1\$4) with signal)	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:00
S42	803	S31 and (delay\$4 near line) and ((dac latch switch samp1\$4) with (timing signal))	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:01
S43	812	S31 and (delay\$4 near line) and ((dac latch switch samp1\$4) with (timing signal clock))	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:01
S44	394	S31 and ((delay\$4 near line) same ((dac latch switch samp1\$4) with (timing signal clock)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:02
S45	13	S44 and dll	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:06
S46	613	(sampling with clock) and dll	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:07
S47	47	S31 and S46	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/11 12:07
S48	2	("5926053").PN.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 11:29
S49	2	("6275547").PN.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 15:06

S50	1476	inverter and ((delay near line) same (latch sampl\$4))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:08
S51	1255	S50 and clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:07
S52	887	inverter and ((delay near line) with (latch sampl\$4))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:09
S53	1789	inverter with (delay near line)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:11
S54	320	S52 and S53	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:09
S55	290	S52 and S53 and clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/09 15:09
S56	3690	(delay near line) with outputs	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 15:11
S57	164	S55 and S56	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 15:13
S58	2	("5796313").PN.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/17 11:01
S59	1322581	pattern	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 11:01
S60	0	pattern and S58	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 11:01
S61	607	pattern and inverters and (ring near oscillat\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 11:02

S62	11	pattern and (odd near inverters) and (ring near oscillat\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 11:06
S63	77	(odd near inverters) and (ring near oscillat\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/17 11:20
S64	30303	(delay near (circuit line)) and (latch sampling adc)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:18
S65	1129	327/152,153,161,158.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 13:46
S66	463	S64 and S65	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 13:46
S67	432	(delay near (circuit line)) same outputs and S66	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:02
S68	0	S67 and @ad <= "04172000"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 13:49
S69	252	S67 and @ad <= "20000417"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:02
S70	212	(delay near (circuit line)) same data same outputs and S66	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:19
S71	124	S70 and @ad <= "20000417"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:19
S72	102	S71 and (latch)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:11
S73	16373	(delay near (circuit line)) and (latch)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:18

S74	6982	(delay near (circuit line)) same (latch)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:18
S75	5709	S74 and @ad <= "20000417"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:19
S76	2341	(delay near (circuit line)) same data same outputs and S75	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:20
S77	1980	(delay near (circuit line)) same data same latch same outputs and S75	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:27
S78	395	sampling and synchro\$7 and S77	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/01 14:28